

SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS

SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS

Surface mount technology (SMT) is used to mount electronic components on the metallized surface of printed wiring boards (PWB) or substrates. SMT makes it possible to mount components on one or both sides of the printed wiring assembly (PWA), producing more reliable electronic assemblies at greatly reduced weight, volume, and cost.

PREFERRED

The solder joint surface is smooth, nonporous, undisturbed, with a finish varying from satin to bright. The fillet completely wets all elements to the periphery of the connection and is concave.

[NASA-STD-8739.2 \[12.8.1 \]](#)

PREFERRED

Part markings are visible and oriented uniformly.

[NASA-STD-8739.2 \[8.7.4.a \]](#), [\[12.7.1.b \]](#), [\[12.8.1.f \]](#)

ACCEPTABLE CHIP-OUTS (NICKS)

Chip-outs (nicks) of the top surface (adhesive coating), less than 0.25mm from the component edge are acceptable. Chips in the component body, element area, or termination area are unacceptable.

[NASA-STD-8739.2 \[8.7.4.b \]](#), [\[8.8.2 \]](#)

UNACCEPTABLE CHIP-OUTS (NICKS)

The use of chip-scale parts with chips in the component body or termination area, and any resistive elements with chip outs, is prohibited.

[NASA-STD-8739.2 \[12.8.2.a.3 \]](#)

SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)

ACCEPTABLE PITS

A solder pit is acceptable, provided the bottom of the cavity can be seen from all angles of vision.

[NASA-STD-8739.2 \[3.1 \]](#), [\[12.8.2.b.5 \]](#)

ACCEPTABLE SMOOTH TOOL IMPRESSION MARKS

Smooth tool impression marks (slight cuts, nicks, scratches or scrapes) on the conductor surface, which do not expose base metal or reduce cross-sectional area are acceptable.

[NASA-STD-8739.2 \[12.8.2.a.4 \]](#)

ACCEPTABLE TILT

Part tilt shall not exceed 25% of component height (H) or diameter (i.e.: MELFs), and shall not interfere with the proper placement of adjacent parts.

[Best Workmanship Practice](#)

UNACCEPTABLE EXCESS TILT

Excessive tilting of a component may impact the long-term reliability and integrity of the solder termination, and may interfere with the proper placement and thermal profile of adjacent parts.

[Best Workmanship Practice](#)

UNACCEPTABLE ADHESIVE INCLUSION

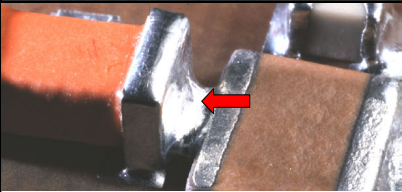
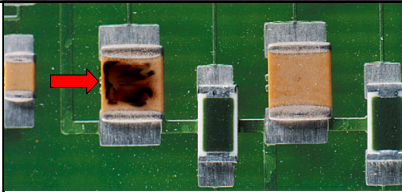
Adhesive material in the solder joint shall be cause for rejection.

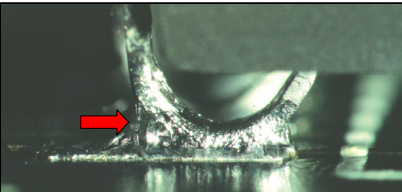
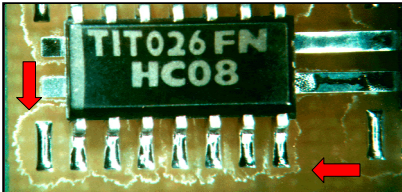
[NASA-STD-8739.2 \[8.10.3 \]](#), [\[12.8.2.b.9 \]](#)

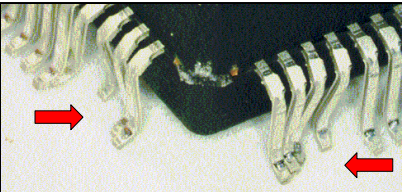
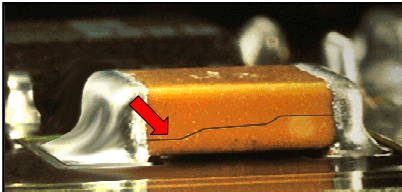
UNACCEPTABLE BLOWHOLE

Blowholes are typically caused by trapped gases or flux during the formation of the solder fillet, and are unacceptable.

[NASA-STD-8739.2 \[12.8.2.b.5 \]](#)

SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>UNACCEPTABLE BRIDGING</p> <p>Bridging is an indicator of poor process controls (i.e.: excess solder, smeared paste, improper placement, incorrect heat).</p> <p>NASA-STD-8739.2 [12.8.2.c.4]</p>	 <p>UNACCEPTABLE CHARRING</p> <p>Charring of components and/or laminate is an indicator of poor process control (i.e.: excessive heat).</p> <p>NASA-STD-8739.2 [12.8.2.a.3]</p>

 <p>UNACCEPTABLE COLD SOLDER JOINT</p> <p>A cold solder joint is an indicator of incorrect process control (i.e.: inadequate heat).</p> <p>NASA-STD-8739.2 [12.8.2.b.1]</p>	 <p>UNACCEPTABLE CONTAMINATION</p> <p>Contamination is a reliability concern. Residual flux and other contaminants can lead to corrosion and circuit failure.</p> <p>NASA-STD-8739.2 [12.8.2.b.9]</p>
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 <p>UNACCEPTABLE COPLANARITY</p> <p>Improper coplanarity of leaded parts will result in bridging, shorts, and misalignment. Parts shall be reworked prior to installation.</p> <p>NASA-STD-8739.2 [7.1]</p>	 <p>UNACCEPTABLE CRACKS (COMPONENT)</p> <p>Cracks (especially in ceramic components) are an indicator of poor process control (i.e.: improper preheat, thermal / mechanical shock, etc.).</p> <p>NASA-STD-8739.2 [12.8.2.a.3]</p>
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NASA WORKMANSHIP STANDARDS



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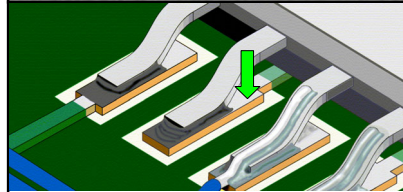
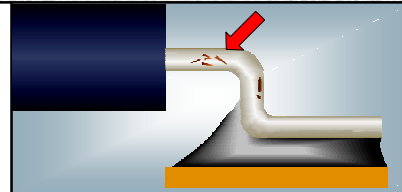
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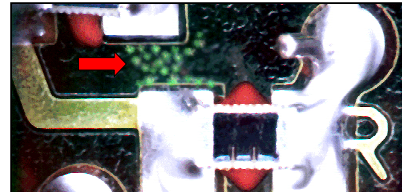
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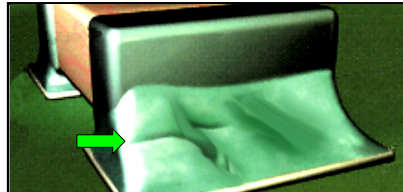
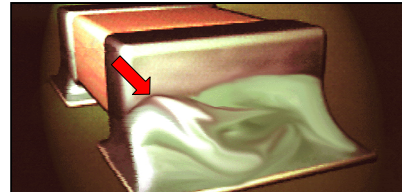
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SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>ACCEPTABLE EXPOSED BASE METAL</p> <p>Exposed base metal on the vertical edges of circuit traces, lands, and pads is acceptable.</p> <p>NASA-STD-8739.2 [12.8.2.c.5]</p>	 <p>UNACCEPTABLE EXPOSED BASE METAL</p> <p>Exposed base metal is prohibited, except for the vertical edges of circuit traces, lands, and pads.</p> <p>NASA-STD-8739.2 [12.8.2.c.5]</p>

 <p>ACCEPTABLE MEASLING</p> <p>Whitish, discrete spots or crosses below the laminate surface - usually induced by thermal shock / stress. Measling that bridges uncommon conductors is unacceptable.</p> <p>NASA-STD-8739.2 [12.8.2.c.3]</p>	 <p>UNACCEPTABLE MEASLING</p> <p>Measling that bridges uncommon conductors is unacceptable.</p> <p>NASA-STD-8739.2 [12.8.2.c.3]</p>
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 <p>ACCEPTABLE NONUNIFORM / UNEVEN FLOW / REFLOW</p> <p>A solder fillet exhibiting a nonuniform / uneven flow line is acceptable, provided there is evidence of good wetting.</p> <p>NASA-STD-8739.2 [12.8.1.g]</p>	 <p>UNACCEPTABLE NONUNIFORM / UNEVEN FLOW / REFLOW</p> <p>The uneven flow / reflow of solder is typically caused by an inadequate / uneven application of heat. The condition is acceptable if good wetting is evident.</p> <p>NASA-STD-8739.2 [12.8.1.g], [12.8.2.b.4]</p>
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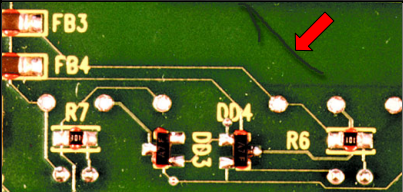
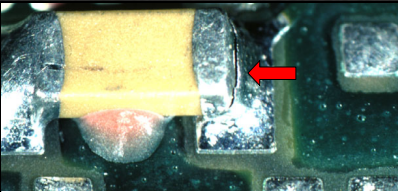
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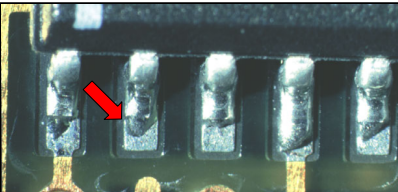
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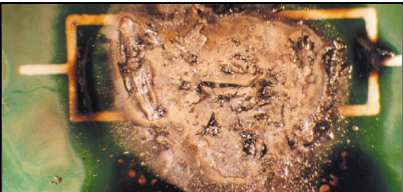
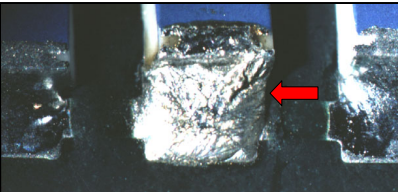
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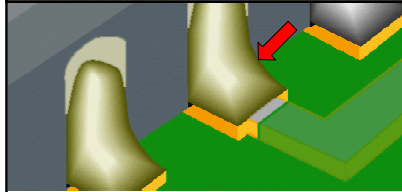
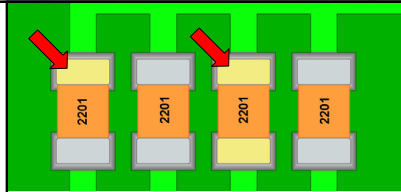
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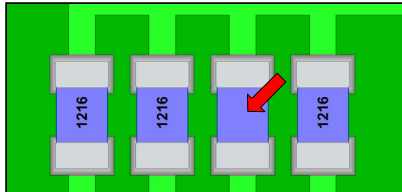
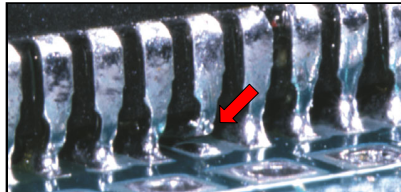
SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>UNACCEPTABLE CRACKS (LAMINATE)</p> <p>Cracks in the laminate are a reliability concern and are a cause for rejection.</p> <p>Best Workmanship Practice</p>	 <p>UNACCEPTABLE CRACKS (SOLDER FILLET)</p> <p>Cracks or fractures in the solder fillet are an indication of mechanical / thermal shock, or temperature coefficient mismatch.</p> <p>NASA-STD-8739.2 [12.8.2.b.3]</p>

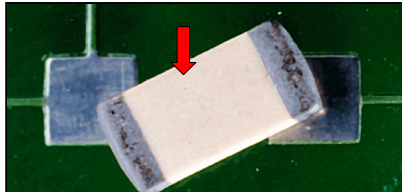
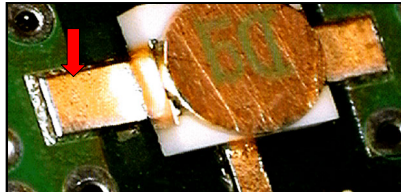
 <p>UNACCEPTABLE DAMAGED PART SEAL</p> <p>Parts with damaged seals shall not be used.</p> <p>NASA-STD-8739.2 [12.7.2.b]</p>	 <p>UNACCEPTABLE DEWETTING</p> <p>Dewetting is an indicator of poor process control (i.e.: excessive heat dwell following reflow).</p> <p>NASA-STD-8739.2 [12.8.2.b.10]</p>
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 <p>UNACCEPTABLE DISCOLORED LAMINATE (BURNS)</p> <p>Burns that physically damage the laminate surface or the assembly are not allowed. Slight discoloration is allowable.</p> <p>NASA-STD-8739.2 [12.8.2.c.2]</p>	 <p>UNACCEPTABLE DISTURBED SOLDER</p> <p>A disturbed solder joint is an indicator of improper process control.</p> <p>NASA-STD-8739.2 [12.8.2.b.3]</p>
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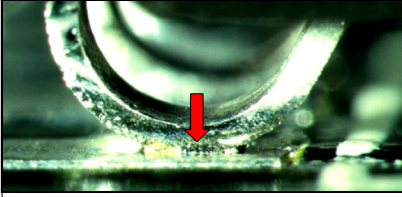
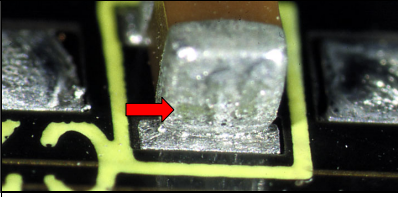
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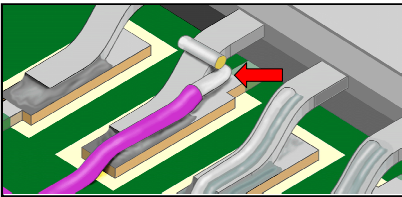
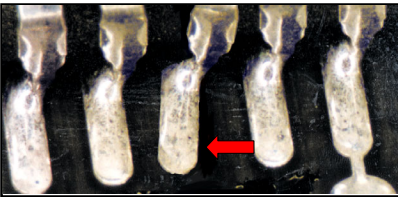
SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>UNACCEPTABLE GOLD INTERMETALLIC</p> <p>Gold intermetallic can severely embrittle a solder joint.</p> <p>NASA-STD-8739.2 [12.8.2.b.22]</p>	 <p>UNACCEPTABLE GOLD PLATING</p> <p>Gold plated surfaces that will become a part of the finished solder connection shall be tinned prior to soldering to remove the gold plating.</p> <p>NASA-STD-8739.2 [7.2.1.b]</p>

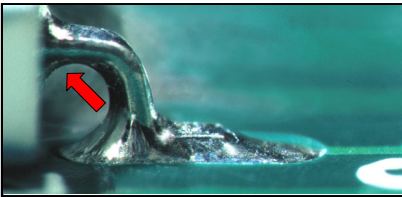
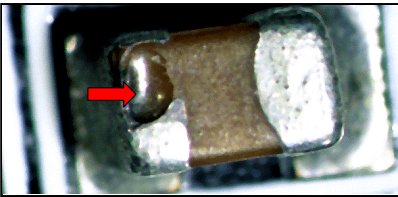
 <p>UNACCEPTABLE IDENTIFICATION MARKS MISSING</p> <p>Parts shall be mounted with the identification markings visible.</p> <p>NASA-STD-8739.2 [8.7.4.a], [12.6.3.1]</p>	 <p>UNACCEPTABLE IMPROPER LEAD SPACING</p> <p>Leads shall not exhibit spacing separation in excess of 0.26mm (0.010 inch) above the solder land.</p> <p>NASA-STD-8739.2 [12.8.2.a.10]</p>
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 <p>UNACCEPTABLE IMPROPER ORIENTATION</p> <p>Parts shall be mounted parallel to the laminate surface, right side up, and aligned to the lands within design and engineering specifications.</p> <p>NASA-STD-8739.2 [8.7.4], [12.7.1.b]</p>	 <p>UNACCEPTABLE IMPROPER TINNING</p> <p>Tinned surfaces shall exhibit at least 95% coverage.</p> <p>NASA-STD-8739.2 [7.2], [12.8.2.a.1]</p>
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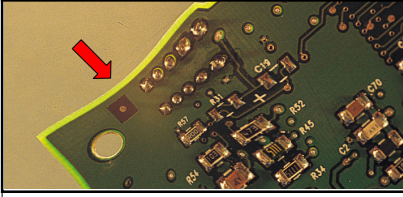
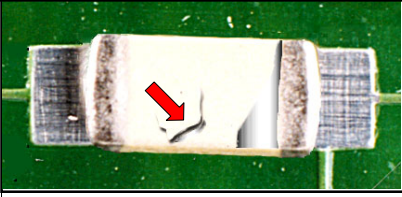
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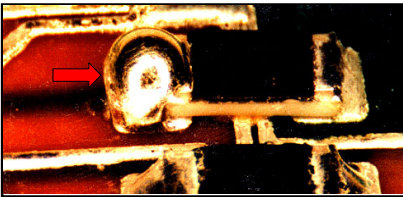
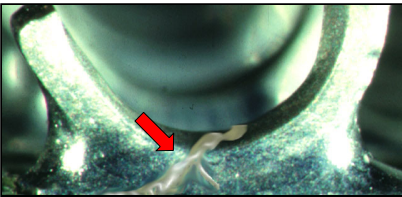
SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>UNACCEPTABLE INSUFFICIENT SOLDER</p> <p>Insufficient solder is an indicator of improper process control, and may result in reduced reliability. In this example, there is no side or heel fillet.</p> <p>NASA-STD-8739.2 [12.8.2.b.6]</p>	 <p>UNACCEPTABLE LEACHING</p> <p>Parts exhibiting leaching or loss of metallization in the termination area shall be rejected.</p> <p>NASA-STD-8739.2 [12.9.1.b.6]</p>

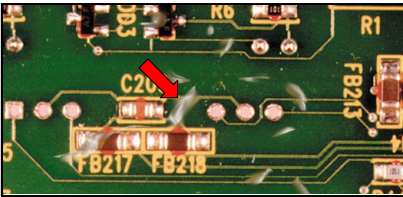
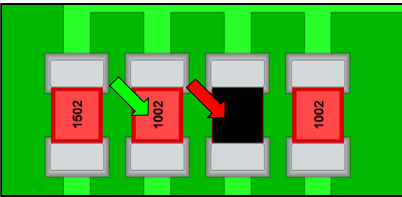
 <p>UNACCEPTABLE LEADS USED AS TERMINALS</p> <p>Part leads shall not be used as terminals, except when the part lead is used as a terminal.</p> <p>NASA-STD-8739.2 [12.8.2.a.9]</p>	 <p>UNACCEPTABLE LIFTED PAD / TRACE</p> <p>Termination pads or traces exhibiting separation from the substrate shall be cause for rejection.</p> <p>NASA-STD-8739.2 [12.8.2.c.1], [12.8.2.c.9]</p>
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 <p>UNACCEPTABLE MENISCUS CONTACT</p> <p>Parts exhibiting contact with, or embedment of, the meniscus and the solder joint, shall be rejected.</p> <p>NASA-STD-8739.2 [12.8.2.b.12]</p>	 <p>UNACCEPTABLE NICKS</p> <p>The use of parts with nicks in the component body or termination area is prohibited.</p> <p>NASA-STD-8739.2 [12.6.3.2], [12.8.2.a.3]</p>
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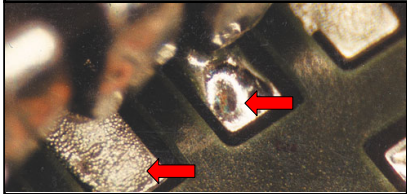
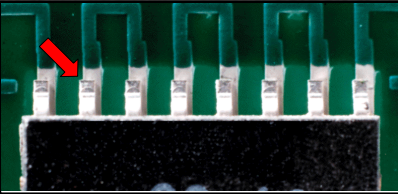
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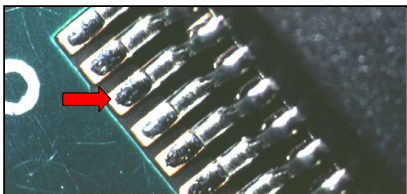
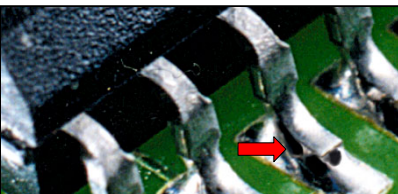
SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>UNACCEPTABLE EXCESSIVE BOW / TWIST (PWB)</p> <p>Excessive bow or twist may inhibit proper mounting and may result in mechanical interference or shorting to adjacent assemblies or chassis.</p> <p>Best Workmanship Practice</p>	 <p>UNACCEPTABLE EXPOSED DIE / CIRCUIT ELEMENTS</p> <p>The unprotected exposure of die or circuit elements is not allowed unless specified in the engineering documentation.</p> <p>NASA-STD-8739.2 [8.8.2]</p>

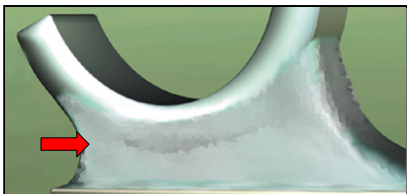
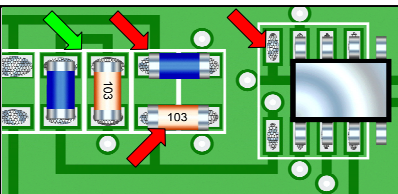
 <p>UNACCEPTABLE EXCESS SOLDER</p> <p>The solder fillet shall exhibit a positive wetting angle and shall not contact the component body.</p> <p>NASA-STD-8739.2 [12.8.2.b.12]</p>	 <p>UNACCEPTABLE FLUX RESIDUE</p> <p>Flux residue indicates improper / incomplete cleaning.</p> <p>NASA-STD-8739.2 [12.8.2.a.5]</p>
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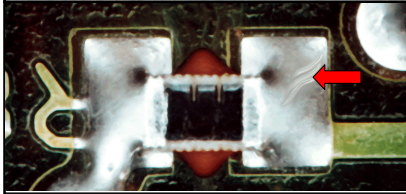

 <p>UNACCEPTABLE FLUX SPLATTER</p> <p>Flux splatter is an indication of an improper process parameter (heat / moisture).</p> <p>NASA-STD-8739.2 [12.8.2.b.7]</p>	 <p>UNACCEPTABLE GLASS SIDE DOWN</p> <p>Thick film components shall be mounted with the protective glass film side in the up position.</p> <p>NASA-STD-8739.2 [8.8.3], [12.7.1]</p>
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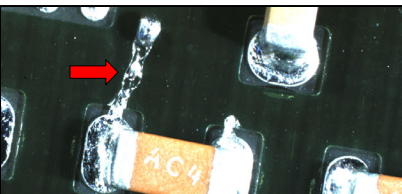
SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>UNACCEPTABLE NO FLOW / REFLOW</p> <p>The lack of proper flow / reflow of solder paste / preforms is an indicator of poor process control or layout design (i.e.: inadequate heat, shadowing). NASA-STD-8739.2 [12.8.1]</p>	 <p>UNACCEPTABLE NO SOLDER</p> <p>The lack of solder is an indicator of poor process control. NASA-STD-8739.2 [12.6.1.a.4], [12.8.2.b.6]</p>

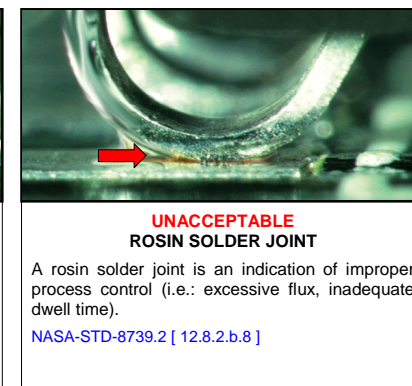
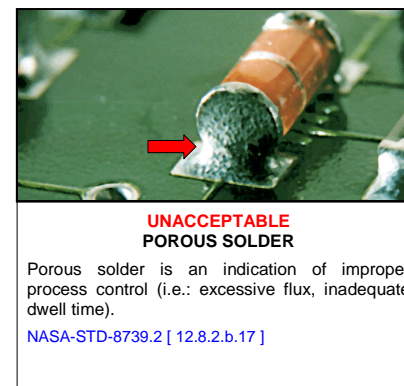
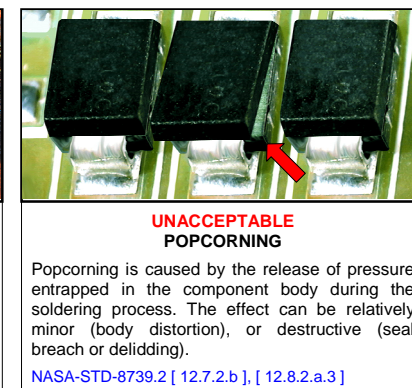
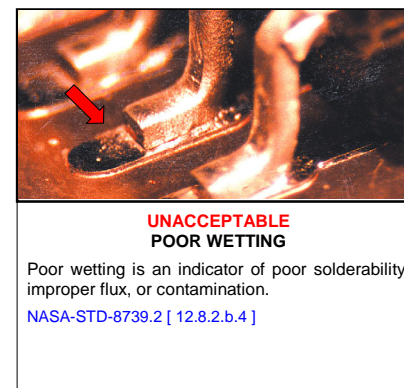
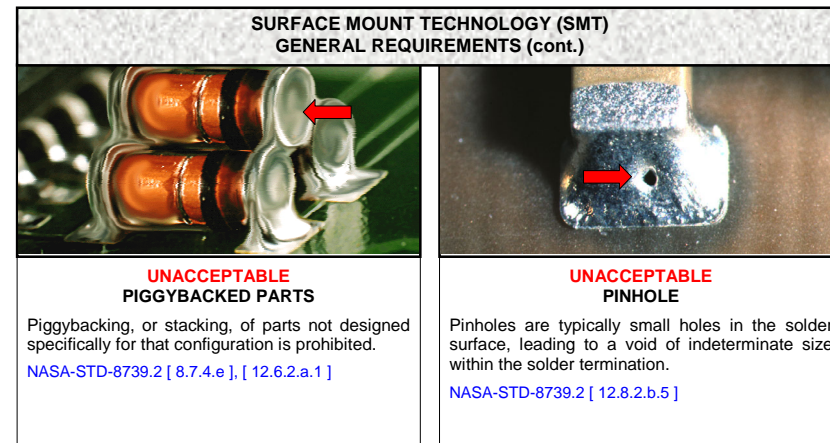
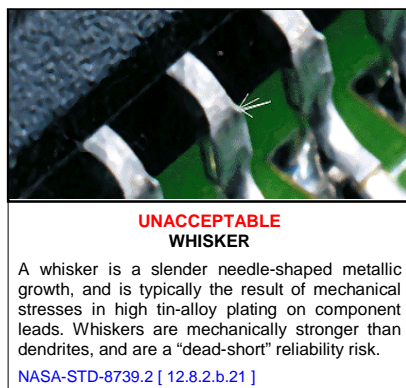
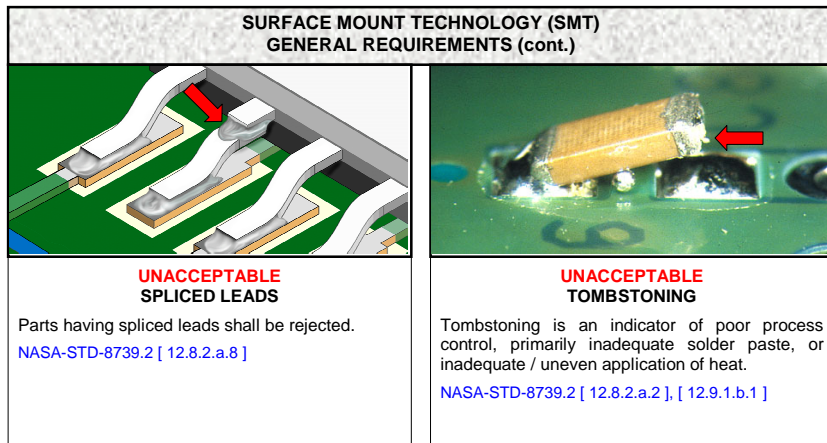
 <p>UNACCEPTABLE NONWETTING</p> <p>Nonwetting is an indicator of poor solderability or contamination. NASA-STD-8739.2 [12.8.2.b.11]</p>	 <p>UNACCEPTABLE OPENS / VOIDS</p> <p>Opens / voids are an indicator of insufficient solder, solder wicking, and/or coplanarity problems. NASA-STD-8739.2 [12.8.2.b.5], [12.8.2.b.6]</p>
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 <p>UNACCEPTABLE OVERHEATED SOLDER</p> <p>Overheated solder has a dull, gray, frosty, and/or crystallized appearance and is the result of excessive exposure to heat. NASA-STD-8739.2 [12.8.2.b.2]</p>	 <p>UNACCEPTABLE PART MISALIGNMENT</p> <p>Part misalignment is an indicator of improper process control. NASA-STD-8739.2 [8.7.4], [12.8.2.a.2]</p>
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SURFACE MOUNT TECHNOLOGY (SMT) GENERAL REQUIREMENTS (cont.)	
 <p>UNACCEPTABLE SCRATCHES (SOLDER FILLET)</p> <p>Scratches in the solder are prohibited. NASA-STD-8739.2 [12.8.2.b.14]</p>	 <p>UNACCEPTABLE SOLDER BALLS / SOLDER FINES</p> <p>Solder balls or fines are an indication of improper process control (inadequate preheat), and/or the use of outdated solder / flux. NASA-STD-8739.2 [12.8.2.b.19]</p>

 <p>UNACCEPTABLE SOLDER IN STRESS RELIEF BEND</p> <p>Solder shall not extend into the stress relief bend of any leaded part. In this example, the solder is also in contact with the part body and the body seal. NASA-STD-8739.2 [12.8.2.b.16]</p>	 <p>UNACCEPTABLE SOLDER PEAKS, ICICLES, SHARP EDGES</p> <p>Solder peaks, icicles, and/or sharp edges are an indicator of an improper process parameter and are a reliability and short-circuit concern. NASA-STD-8739.2 [12.8.2.c.4]</p>
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 <p>UNACCEPTABLE SOLDER SLIVERS</p> <p>Solder slivers are an indication of improper process control, and are a reliability and short-circuit concern. NASA-STD-8739.2 [12.8.2.b.20]</p>	 <p>UNACCEPTABLE SOLDER WEBBING</p> <p>Webbing is an indication of improper process control, and is a reliability and short-circuit concern. NASA-STD-8739.2 [12.8.2.b.18]</p>
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